

EAST - [10085735.wsp:1]

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BRS:

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- L1: (67447) "455"/\$ccls.
- L2: (6168) L1 and (gain near3 control\$4)
- L3: (1816) L2 and ("power amplifier" or PA)
- L4: (9) L3 and (power near5 ratio near5 monitor\$3)
- L5: (6) L4 and (variable near5 gain)
- L6: (11750) (variable near3 gain) and (gain near3 control\$4)
- L7: (2435) 6 and ("power amplifier" or PA)
- L8: (6) 7 and "second order distortion"
- L9: (2) 8 and (squar\$3 near3 circuit)
- L10: (3) 8 and squar\$3
- L11: (22) 7 and ("second order" near5 distortion)
- L12: (1) 11 and BPF
- L13: (12) 11 and "band pass filter"
- L14: (11) 13 and multiplier
- L15: (0) 14 and subtractor
- L16: (11) 14 and subtract\$3
- L17: (4) 14 and subtract\$3
- L18: (4) 17 and wireless
- L19: (4) 18 and antenna
- L20: (4) 19 and transmitter
- L21: (4) 20 and squar\$3

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20 and squar\$3

	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current X	Ret	Inventor	IS
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6803829 B2	20041012	149	Integrated VCO having an improved tuning range over process and temperature	331/34	257/E27.046		Duncan; Ralph et al.	<input checked="" type="checkbox"/>
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6759904 B2	20040706	123	Large gain range, high linearity, low noise MOS VGA	330/254	257/E27.046		Behzad; Arya R.	<input checked="" type="checkbox"/>
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6525609 B1	20030225	123	Large gain range, high linearity, low noise MOS VGA	330/254	257/E27.046		Behzad; Arya R.	<input checked="" type="checkbox"/>
4	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6426680 B1	20020730	152	System and method for narrow band PLL tuning	331/34	257/E27.046		Duncan; Ralph et al.	<input checked="" type="checkbox"/>

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- Drafts
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- L6: (11750) (variable near3 gain) and (gain near3 control\$4)
- L7: (2435) 6 and ("power amplifier" or PA)
- L8: (6) 7 and "second order distortion"
- L9: (2) 8 and (square3 near3 circuit)

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- S1: (1) ("6115406").PN.
- S2: (67447) "455"/\$.ccls.
- S3: (6168) S2 and (gain near3 control\$4)
- S4: (1816) S3 and ("power amplifier" or PA)
- S5: (9) S4 and (power near5 ratio near5 monitor\$3)
- S6: (6) S5 and (variable near5 gain)

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United States
Patent Application Publication
Hayashihara
US 2002/0123315 A1
Sep. 5, 2002

(54) RADIO COMMUNICATION TERMINAL AND
GAIN CONTROL CIRCUIT FOR THE SAME

(71) Inventor: Mikio Hayashihara, Wakayama-shi, Japan

(73) Assignee: NEC CORPORATION, Nakatsugawa-cho, Tsurumi-ku, Yokohama, Japan

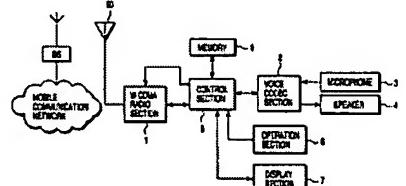
(21) Appl. No.: 10/085,735
(22) Filed: Mar. 5, 2002

(19) Foreign Application Priority Data
Mar. 5, 2001 (JP) 2001-099714

(72) Pub. Cl.: H04B 7/16
(74) Int. Cl.: H04B 7/16

(75) ABSTRACT

A radio communication device has a transmission power amplifier that amplifies a transmission signal in a predetermined band. An object thereof is to provide an audio feed-back circuit which can reduce a feedback signal from a microphone to a minimum level without causing a feedback loop. The present invention provides a radio communication device which has a transmission power amplifier that amplifies a transmission signal in a predetermined band, and a feedback signal from a microphone to a minimum level without causing a feedback loop. The present invention provides a radio communication device which has a transmission power amplifier that amplifies a transmission signal in a predetermined band, and a feedback signal from a microphone to a minimum level without causing a feedback loop.



[BRS Form] [ISIR Form] [Image] [Text] [HTML]

NU	1	Document ID	Issue Date	Pages	Title	Current OR	Current X	Refn	Inventor	IS
1	<input type="checkbox"/>	US 20030186664	20031002	16	Distortion reduction calibration	455/232.1	455/234.1; 455/236.1		Shah, Peter Jivan	<input checked="" type="checkbox"/>
2	<input type="checkbox"/>	US 20020123315	20020905	9	Radio communication terminal and gain control circuit for the same	455/194.2	455/241.1		Hayashihara, Mikio	<input checked="" type="checkbox"/>

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L3: (6168) 2 and (gain near3 control\$4)
L4: (1816) 3 and ("power amplifier" or PA)
L5: (9) 4 and (power near5 ratio near5 monitor\$3)
L6: (6) 5 and (variable near5 gain)

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 DOS USPCFUBUSPATEPRO Both
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 5 and (variable near5 gain)

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	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current X	Reu	Inventor	S
1	<input type="checkbox"/>	<input type="checkbox"/>	US 20020123315	20020905	9	Radio communication terminal and gain control circuit for the same A1	455/194.2	455/241.1		Hayashihara, Mikio	<input checked="" type="checkbox"/>
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6759902 B2	20040706	11	Single-detector automatic gain control circuit	330/136	330/279; 455/126		Kossor, Michael G.	<input checked="" type="checkbox"/>
3	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5465399 A	19951107	29	Apparatus and method for controlling transmitted power in a radio network	455/69	455/522; 455/67.11;		Oberholtzer; John C. et al.	<input checked="" type="checkbox"/>
4	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5182527 A	19930126	23	Power amplifying apparatus for wireless transmitter	330/285	330/129; 330/279;		Nakanishi; Euchi et al.	<input checked="" type="checkbox"/>
5	<input type="checkbox"/>	<input type="checkbox"/>	US 5126688 A	19920630	24	Power amplifying apparatus for wireless transmitter	330/285	330/127; 330/129;		Nakanishi; Euchi et al.	<input checked="" type="checkbox"/>
6	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 4697187 A	19870929	6	Precipitation loss compensation and disablement for avoidance of satellite	342/358	342/353; 455/13.4;		Olmo; Satoru et al.	<input checked="" type="checkbox"/>

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